



APPLICANT'S ART CITATION (Use several sheets if necessary)		Application 09/821,444		OFGS File No P/2041-58									
		Applicant SAKAI, J.											
		Filing Date 3/29/01		Group Art Unit __									
U.S. PATENT DOCUMENTS													
Examiner Initials	Document Number							Date	Name	Class	Sub-class	Filing Date If Appropriate	
FOREIGN PATENT DOCUMENTS													
	Document Number							Date	Country	Class	Sub-class	Translation	
												Yes	No
#5	MX	10-	0	7	8	8	8	0	3/1998	JAPAN		Abst	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)													
#1		Torii et al., "Control Parallel On-Chip Multi-processor: MUSCAT," JSPP '97 Joint Symposium on Parallel Processing 1997, the Information Processing Society of Japan, pp. 229-236, May, 1997. (SEE DISCUSSION IN SPECIFICATION)											
#2	MX	Torii et al., "On-Chip Control Parallel Multi-processor: MUSCAT," IPSJ TRANSACTION, Vol. 39, No. 6, June, 1998. (SEE DISCUSSION IN SPECIFICATION)											
#4	MX	Sakai et al., "Automatic Parallelizing Method for Control-parallel Multi-threaded Architecture," IPSJ TRANSACTION, Vol. 40, No. 5, May, 1999. (SEE DISCUSSION IN SPECIFICATION)											
#5		A. V. Aho et al., "Compilers II, Principles, Techniques, and Tools," translated into Japanese by Kenichi Harada, SAIENSE-SHA, 1990, pp. 734-741. (SEE DISCUSSION IN SPECIFICATION)											
Examiner <i>M. M. M.</i>		Date Considered 11-22-2004											
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